

Notice of Allowability

Application No.

10/648,853

Examiner

Michael t. Tran

Applicant(s)

KANDA ET AL.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to communications filed February 21, 2006 through March 10, 2006.
2. ☒ The allowed claim(s) is/are 1 and 3-25.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

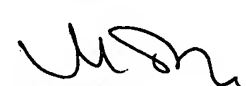
* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 031006
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.


MICHAEL
PRIMARY EXAMINER

DETAILED ACTION

1. In response to the Communication dated February 21, 2006 through March 10, 2006, claims 1 and 3-25 are active in this application.

Drawings

2. The drawings filed August 26, 2003 have been approved.

Allowable Subject Matter

3. Claims 1 and 3-25 are allowable over the prior art of record.

4. The following is an Examiner's statement of reasons for the indication of allowable subject matter: the prior art of records does not show (in addition to other elements in the claim) the following:

- A delay circuit which is connected to the power-on level detecting circuit and the at least one pad and which has delay time controlled according to the control signal so as to delay the power-on reset signal; and a rom region which stores fuse data, the rom region connected to the delay circuit, a timing for activating an operation of reading the fuse data is controlled according to the power-on reset signal output from the delay circuit.
- A read control circuit connected to the rom region to receive the first fuse data and control reading of the second fuse data from the rom region after the power supply voltage has reached a second level during a rise so as to control timing

for activating an operation of reading the second fuse data, according to the first fuse data.

- A delay circuit which receives and delays the pulse signal and has delay time controlled on the basis of a second signal supplied to at least one second pad; and a rom read control circuit connected to the rom region and the delay circuit to control reading of the fuse data from the rom region according to an output from the delay circuit.
 - At least two second non-volatile memory chips having a rom region which stores fuse data, the at least two second non-volatile memories each receiving the power supply voltage and controlling reading of the fuse data from the rom region after the power supply voltage has reached a predetermined level during a rise, the at least two second non-volatile memory chips having timings for activating an operation of reading the fuse data which timings are different from that for the first non-volatile memory chip and from each other
 - A second pulse generating circuit which generates a second pulse signal on the basis of the first signal; a second delay circuit which receives and delays the second pulse signal and has delay time for the second pulse signal controlled on the basis of a third signal supplied to at least one third pad; and a second rom read control circuit which receives an output from the second delay circuit to control reading of the fuse data from the second rom region according to the output from the second delay circuit.
5. Any comments considered necessary by applicant must be submitted no later

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than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael T. Tran whose telephone number is (571) 272-1795.

7. Any inquiry of a general nature or relating to the status of this application should be directed to Group receptionist whose telephone number is (571) 272-1650.



Michael T. Tran

May 1, 2006

MICHAEL TRAN
PRIMARY EXAMINER